

## Patent Abstracts of Japan

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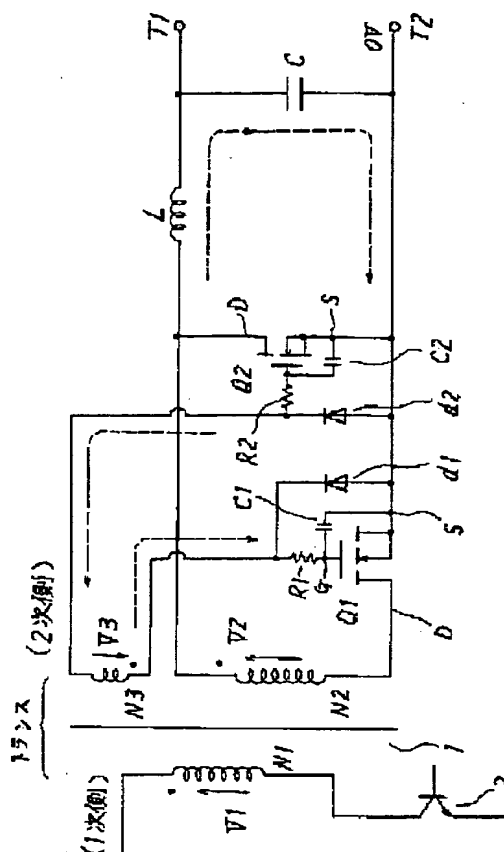
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TITLE : SYNCHRONOUS RECTIFICATION  
SWITCHING REGULATOR



**ABSTRACT :** PURPOSE: To realize a circuit which prevents a current from flowing improperly in a fault, which can supply power stably and which can be driven and controlled with high efficiency by the small number of components even when the circuit is used as a power supply circuit with redundancy.

**CONSTITUTION:** A circuit is provided with a transformer, with MOSFETs Q1, Q2, with a coil L and with a capacitor C, sources for the MOSFETs Q1, Q2 are connected in common, and drains for them are connected to respective terminals for a secondary winding N2. In the circuit, a tertiary winding N3 is installed in the transformer, a gate control circuit which ON/OFF-controls the MOSFETs Q1, Q2 simultaneously is connected to the winding N3, and the two MOSFETs can be controlled by one winding. In addition, diodes d1, d2 and the like are installed in the gate control circuit.

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